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	Filing Date		2002-02-27	
	First Named Inventor	Hiroshi HASHIMOTO		
	Art Unit	2814		
	Examiner Name	Wai Sing Louie		
	Attorney Docket Number	020244		

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/W.L./	1	International Business Machines, "A single-layer polysilicon EEPROM memory cell using diffusion gate"; January 1, 1999; Research Disclosure, Mason Publications, Hampshire, GB, XP007123866; ISSN: 0374-4353.	<input type="checkbox"/>
/W.L./	2	European Search Report; European Patent Application No. EP 08105801.8-1235, dated December 22, 2008	<input type="checkbox"/>

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